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1 Group testing with DNA chips: generating designs and decoding experiments

Schliep, A.; Torney, D.C.; Rahmann, S.;

Bioinformatics Conference, 2003. CSB 2003. Proceedings of the 2003 IEEE , 11-14 Aug. 2003

Pages:84 - 91

[\[Abstract\]](#) [\[PDF Full-Text \(300 KB\)\]](#) **IEEE CNF**

2 Conference Record of Thirty-Fifth Asilomar Conference on Signals, Systems and Computers (Cat.No.01CH37256)

Signals, Systems and Computers, 2001. Conference Record of the Thirty-Fifth Asilomar Conference on , Volume: 1 , 4-7 Nov. 2001

[\[Abstract\]](#) [\[PDF Full-Text \(265 KB\)\]](#) **IEEE CNF**

3 System level design, a VHDL based approach

van den Hurk, J.; Dilling, E.;

Design Automation Conference, 1995, with EURO-VHDL, Proceedings EURO-DAC '95., European , 18-22 Sept. 1995

Pages:568 - 573

[\[Abstract\]](#) [\[PDF Full-Text \(528 KB\)\]](#) **IEEE CNF**

4 High performance, high throughput turbo/SOVA decoder design

Zhongfeng Wang; Parhi, K.K.;

Communications, IEEE Transactions on , Volume: 51 , Issue: 4 , April 2003

Pages:570 - 579

[\[Abstract\]](#) [\[PDF Full-Text \(444 KB\)\]](#) **IEEE JNL**

5 Effect of transition noise on turbo decoding for optical data storage

Song, H.; Vijaya Kumar, B.V.K.;

Optical Data Storage, 2000. Conference Digest , 14-17 May 2000

Pages:188 - 190

[\[Abstract\]](#) [\[PDF Full-Text \(188 KB\)\]](#) [IEEE CNF](#)

6 Probabilistic application modeling for system-level performance analysis

Marculescu, R.; Nandi, A.;

Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001.

Proceedings , 13-16 March 2001

Pages:572 - 579

[\[Abstract\]](#) [\[PDF Full-Text \(724 KB\)\]](#) [IEEE CNF](#)

7 Exploring design space of parallel realizations: MPEG-2 decoder case study

Dwivedi, B.K.; Hoogerbrugge, J.; Stravers, P.; Balakrishnan, M.;

Hardware/Software Codesign, 2001. CODES 2001. Proceedings of the Ninth International Symposium on , 25-27 April 2001

Pages:92 - 97

[\[Abstract\]](#) [\[PDF Full-Text \(508 KB\)\]](#) [IEEE CNF](#)

8 Control unit synthesis targeting low-power processors

Chuan-Yu Wang; Roy, K.;

Computer Design: VLSI in Computers and Processors, 1995. ICCD '95.

Proceedings., 1995 IEEE International Conference on , 2-4 Oct. 1995

Pages:454 - 459

[\[Abstract\]](#) [\[PDF Full-Text \(576 KB\)\]](#) [IEEE CNF](#)

9 Integrated circuits for channel coding in 3G cellular mobile wireless systems

Thomas, C.; Bickerstaff, M.A.; Davis, L.M.; Prokop, T.; Widdup, B.; Gongyu Zhou; Garrett, D.; Nicol, C.;

Communications Magazine, IEEE , Volume: 41 , Issue: 8 , Aug. 2003

Pages:150 - 159

[\[Abstract\]](#) [\[PDF Full-Text \(2292 KB\)\]](#) [IEEE JNL](#)

10 LZW-based code compression for VLIW embedded systems

Chang Hong Lin; Yuan Xie; Wolf, W.;

Design, Automation and Test in Europe Conference and Exhibition, 2004.

Proceedings , Volume: 3 , 16-20 Feb. 2004

Pages:76 - 81 Vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(521 KB\)\]](#) [IEEE CNF](#)

11 Design methods for system-on-a-chip control codecs to enhance performance and reuse

Fisher, J.S.; Murphy, E.J., III; Bibyk, S.B.;

National Aerospace and Electronics Conference, 2000. NAECON 2000. Proceedings of the IEEE 2000 , 10-12 Oct. 2000

Pages:666 - 673

[\[Abstract\]](#) [\[PDF Full-Text \(676 KB\)\]](#) [IEEE CNF](#)

12 Microarchitecture support for improving the performance of load target prediction

Chung-Ho Chen; Wu, A.;
Microarchitecture, 1997. Proceedings. Thirtieth Annual IEEE/ACM International
Symposium on , 1-3 Dec. 1997
Pages:228 - 234

[\[Abstract\]](#) [\[PDF Full-Text \(652 KB\)\]](#) [IEEE CNF](#)

13 Design methods for SOC control CODECS to enhance reuse

Fisher, J.S.; Bibyk, S.B.;
ASIC/SOC Conference, 2001. Proceedings. 14th Annual IEEE International , 12-15
Sept. 2001
Pages:423 - 427

[Abstract] [PDF Full-Text (480 KB)] **IEEE CNF**

14 The decodability of multiple-PRF radar waveforms

Kinghorn, A.M.; Williams, N.K.;
Radar 97 (Conf. Publ. No. 449), 14-16 Oct. 1997
Pages: 544 - 547

[Abstract] [PDF Full-Text (520 KB)] IEE CNF

15 Class-based decompressor design for compressed instruction memory in embedded processors

Weiss, S.; Beren, S.;
Computers, IEEE Transactions on , Volume: 52 , Issue: 11 , Nov. 2003
Pages:1495 - 1500

[Abstract] [PDF Full-Text (761 KB)] IEEE JNL

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